

Gokce Gurun

List of Publications by Year in descending order

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Version: 2024-02-01

15
papers

268
citations

1937685

4
h-index

2272923

4
g-index

15
all docs

15
docs citations

15
times ranked

288
citing authors

#	ARTICLE	IF	CITATIONS
1	3-D real-time volumetric imaging using 20 MHz 1.5-mm diameter single-chip CMUT-on-CMOS array. , 2012, , .		2
2	Thermal-mechanical-noise-based CMUT characterization and sensing. IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, 2012, 59, 1267-1275.	3.0	18
3	An Analog Integrated Circuit Beamformer for High-Frequency Medical Ultrasound Imaging. IEEE Transactions on Biomedical Circuits and Systems, 2012, 6, 454-467.	4.0	37
4	An analog beamformer for integrated high-frequency medical ultrasound imaging. , 2011, , .		3
5	Front-end receiver electronics for high-frequency monolithic CMUT-on-CMOS imaging arrays. IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control, 2011, 58, 1658-1668.	3.0	113
6	High frequency ultrasonic imaging using thermal mechanical noise recorded on capacitive micromachined transducer arrays. Applied Physics Letters, 2011, 99, 224103.	3.3	14
7	Experimental study of dual-ring CMUT array optimization for forward-looking IVUS. , 2011, , .		3
8	A 1.5-mm diameter single-chip CMOS front-end system with transmit-receive capability for CMUT-on-CMOS forward-looking IVUS. , 2011, , .		14
9	Thermal mechanical noise based characterization of CMUTs using monolithically integrated low noise receiver electronics. , 2010, , .		6
10	An annular CMUT array beamforming system for high-frequency side looking IVUS imaging. , 2010, , .		0
11	A large-scale Reconfigurable Smart Sensory Chip. , 2009, , .		15
12	A tunable analog delay element for high-frequency dynamic beamforming. , 2009, , .		11
13	Evaluation of CMUT annular arrays for side-looking IVUS. , 2009, , .		5
14	Front-end CMOS electronics for monolithic integration with CMUT arrays: Circuit design and initial experimental results. , 2008, , .		13
15	Single chip CMUT arrays with integrated CMOS electronics: Fabrication process development and experimental results. , 2008, , .		14