Zijiang Zhang

List of Publications by Year
in descending order

8 papers	79 citations	1684188 5 h-index	2053705 5 g-index
8 all docs	8 docs citations	8 times ranked	53 citing authors

#	Article	IF	Citations
1	A DCâ€“28-GHz 7-Bit High-Accuracy Digital-Step Attenuator in 55-nm CMOS. IEEE Microwave and Wireless Components Letters, 2022, 32, 157-160.	3.2	8
2	A Four-Element 7.5â€“9-GHz Phased-Array Receiver With 1â€“8 Simultaneously Reconfigurable Beams in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 1114-1126.	4.6	17
3	A 6.5â€“12-GHz Balanced Variable-Gain Low-Noise Amplifier With Frequency-Selective Gain Equalization Technique. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 732-744.	4.6	14
4	A DCâ€“Ka-Band 7-Bit Passive Attenuator With Capacitive-Compensation-Based Bandwidth Extension Technique in 55-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 3861-3874.	4.6	8
5	A DC-32GHz 7-Bit Passive Attenuator with Capacitive Compensation Bandwidth Extension Technique in 55 nm CMOS. , 2020, , .		7
6	An 800-ps Origami True-Time-Delay-Based CMOS Receiver Front End for 6.5â€“9-GHz Phased Arrays. IEEE Solid-State Circuits Letters, 2020, 3, 382-385.	2.0	8
7	A 4-Element 7.5-9 GHz Phased Array Receiver with 8 Simultaneously Reconfigurable Beams in 65 nm CMOS Technology. , 2020, , .		10
8	A 6.5-12 GHz Balanced Variable Gain Low-Noise Amplifier with Frequency-Selective Non-Foster Gain Equalization Technique. , 2020, , .		7