

Won-Young Lee

List of Publications by Year in descending order

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Version: 2024-02-01

4
papers

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3311381
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docs citations

4
times ranked

1
citing authors

#	ARTICLE	IF	CITATIONS
1	A digital clock and data strobe aligner for write calibration of dynamic random access memory. Electronics Letters, 2022, 58, 268-270.	1.0	1
2	A Single-Ended Transmitter With Low Switching Noise Injection and Quadrature Clock Correction Schemes for DRAM Interface. IEEE Access, 2022, 10, 52273-52279.	4.2	0
3	A Low EMI Transmitter for DRAM Interface with Quadrature Clock Corrector. , 2021, , .		1
4	On-chip data strobe transmission with short-circuit current protection scheme for dynamic random access memory. Electronics Letters, 2018, 54, 21-23.	1.0	0