

# Seung Yup Jang

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	An Optimal Design for 1.2kV 4H-SiC JBSFET (Junction Barrier Schottky Diode Integrated MOSFET) With Deep P-Well. IEEE Electron Device Letters, 2022, 43, 785-788.	3.9	10
2	Static Performance and Reliability of 4H-SiC Diodes with P+ Regions Formed by Various Profiles and Temperatures. , 2022, , .		4
3	Channel Design Optimization for 1.2-kV 4H-SiC MOSFET Achieving Inherent Unipolar Diode 3 <sup>rd</sup> Quadrant Operation. IEEE Journal of the Electron Devices Society, 2022, 10, 495-503.	2.1	3
4	An Inclusive Structural Analysis on the Design of 1.2kV 4H-SiC Planar MOSFETs. IEEE Journal of the Electron Devices Society, 2021, 9, 804-812.	2.1	14
5	A Static, Switching, Short-circuit Characteristics of 1.2 kV 4H-SiC MOSFETs: Comparison between Linear and (Bridged) Hexagonal Topology. , 2021, , .		0
6	Increased 3rd Quadrant Current Handling Capability of 1.2kV 4H-SiC JBS Diode-Integrated MOSFETs (JBSFETs) with Minimal Impact on the Forward Conduction and Blocking Performances. , 2021, , .		3
7	Demonstration of Cell-to-Cell Integrated 4H-SiC Lateral Bi-Directional Junction Field Effect Transistor (LBiDJFET). , 2021, , .		1