

Sandeep Chandran

List of Publications by Year in descending order

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Version: 2024-02-01

8
papers

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citations

2682572

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2550090

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g-index

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all docs

8
docs citations

8
times ranked

20
citing authors

#	ARTICLE	IF	CITATIONS
1	Managing Trace Summaries to Minimize Stalls During Postsilicon Validation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1881-1894.	3.1	8
2	Architectural Support for Handling Jitter in Shared Memory Based Parallel Applications. IEEE Transactions on Parallel and Distributed Systems, 2014, 25, 1166-1176.	5.6	4
3	DHOOM. , 2019, , .		4
4	A Generic Implementation of Barriers Using Optical Interconnects. , 2016, , .		2
5	Extending trace history through tapered summaries in post-silicon validation. , 2016, , .		1
6	Area-Aware Cache Update Trackers for Postsilicon Validation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1794-1807.	3.1	1
7	Space Sensitive Cache Dumping for Post-silicon Validation. , 2013, , .		0
8	Debug Data Reduction Techniques. , 2019, , 211-229.		0