

Shivendra Singh Parihar

List of Publications by Year in descending order

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2682572

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49
citing authors

#	ARTICLE	IF	CITATIONS
1	Compact Modeling of Negative-Capacitance FDSOI FETs for Circuit Simulations. IEEE Transactions on Electron Devices, 2020, 67, 2710-2716.	3.0	14
2	Compact Model for Geometry Dependent Mobility in Nanosheet FETs. IEEE Electron Device Letters, 2020, 41, 313-316.	3.9	22
3	Compact Modeling of Drain-Extended MOS Transistor Using BSIM-BULK Model. Communications in Computer and Information Science, 2019, , 667-678.	0.5	0