

Rui Xiao

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10064167/publications.pdf>

Version: 2024-02-01

3
papers

5
citations

3311381

1
h-index

2917675

2
g-index

3
all docs

3
docs citations

3
times ranked

0
citing authors

#	ARTICLE	IF	CITATIONS
1	An 8-Bit in Resistive Memory Computing Core With Regulated Passive Neuron and Bitline Weight Mapping. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 379-391.	3.1	4
2	A Customized NoC Architecture to Enable Highly Localized Computing-on-the-Move DNN Dataflow. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 1692-1696.	3.0	1
3	The Impact of Non-linear NVM Devices on In-Memory Computing. , 2019, , .		0