

Yingtao Jiang

List of Publications by Year in descending order

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17
papers

52
citations

2257833

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1872570

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all docs

17
docs citations

17
times ranked

40
citing authors

#	ARTICLE	IF	CITATIONS
1	Detection of and Countermeasure Against Thermal Covert Channel in Many-Core Systems. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 252-265.	1.9	10
2	A 20 GHz high speed, low jitter, high accuracy and wide correction range duty cycle corrector. , 2015, , .		7
3	Performance Optimization of Many-Core Systems by Exploiting Task Migration and Dark Core Allocation. IEEE Transactions on Computers, 2022, 71, 92-106.	2.4	5
4	Modeling and Analysis of Thermal Covert Channel Attacks in Many-core Systems. IEEE Transactions on Computers, 2023, 72, 494-500.	2.4	5
5	An adaptive probabilistic scheduler for offloading time-constrained tasks in local mobile clouds. , 2014, , .		4
6	SoC Scan-Chain verification utilizing FPGA-based emulation platform and SCE-MI interface. , 2014, , .		3
7	An Adaptive PAM-4 Analog Equalizer With Boosting-State Detection in the Time Domain. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 2907-2916.	2.1	3
8	Evaluating Open IaaS Cloud Platforms Based upon NIST Cloud Computing Reference Model. , 2014, , .		2
9	Half baud-rate, low BER PAM-4 CDR based on SS-MMSE algorithm. Electronics Letters, 2016, 52, 2036-2038. 0.5		2
10	A Scalable Parameterized NoC Emulator Built Upon Xilinx Virtex-7 FPGA. , 2017, , .		2
11	On a Consistency Testing Model and Strategy for Revealing RISC Processor's Dark Instructions and Vulnerabilities. IEEE Transactions on Computers, 2022, 71, 1586-1597.	2.4	2
12	Evolution of Publications, Subjects, and Co-Authorships in Network-on-Chip Research From a Complex Network Perspective. IEEE Access, 2021, 9, 149399-149422.	2.6	2
13	IMSC: Instruction set architecture monitor and secure cache for protecting processor systems from undocumented instructions. IET Information Security, 2022, 16, 314-319.	1.1	2
14	High-Performance Password Recovery Hardware going from GPU to Hybrid CPU-FPGA Platform. IEEE Consumer Electronics Magazine, 2020, , 1-1.	2.3	1
15	Efficient On-Chip Multicast Routing based on Dynamic Partition Merging. , 2020, , .		1
16	Detection of Thermal Covert Channel Attacks Based on Classification of Components of the Thermal Signal Features. IEEE Transactions on Computers, 2023, 72, 971-983.	2.4	1
17	Building temperature-insensitive nanoscale CMOS circuits with adaptive voltage power supplies. , 2017, , .		0